The RF Line Wideband Linear Amplifier

Designed for amplifier applications in 50 to 100 ohm systems requiring wide bandwidth, low noise and low distortion. This hybrid provides excellent gain stability with temperature and linear amplification as a result of the push–pull circuit design.

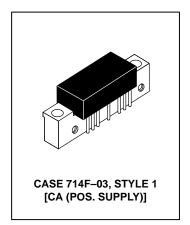
- Specified Characteristics at V_{CC} = 24 V, T_C = 25°C:
 Frequency Range 0.35 to 400 MHz
 Output Power 1000 mW Typ @ 1 dB Compression, f = 200 MHz
 Power Gain 18.5 dB Typ @ f = 50 MHz
 PEP 1000 mW Typ @ -32 dB IMD, f = 200 MHz
 Noise Figure 5 dB Typ @ f = 200 MHz
 ITO 47 dBm Typ @ f = 150 MHz
- All Gold Metallization for Improved Reliability
- Unconditional Stability Under All Load Conditions

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Supply Voltage	Vcc	28	Vdc
RF Power Input	P _{in}	+14	dBm
Operating Case Temperature Range	TC	-20 to +100	°C
Storage Temperature Range	T _{stg}	-40 to +100	°C

CA2818C

18.5 dB 0.35-400 MHz 1000 mWATT WIDEBAND LINEAR AMPLIFIER



ELECTRICAL CHARACTERISTICS (T_C = 25°C, V_{CC} = 24 V, 50 Ω system unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Frequency Range	BW	0.35	_	400	MHz
Gain Flatness (f = 0.35-400 MHz)	FL	_	±0.5	±1	dB
Power Gain (f = 50 MHz)	PG	17.75	18.5	19.25	dB
Noise Figure, Broadband (f = 200 MHz)	NF	_	5	6	dB
Power Output — 1 dB Compression (f = 200 MHz)	P _{o 1dB}	800	1000	_	mW
Third Order Intercept (See Figure 10, f ₁ = 200 MHz)	ITO	43	45	_	dBm
Input/Output VSWR (f = 0.35-400 MHz)	VSWR	_	1.7:1	2:1	_
Second Harmonic Distortion (P_0 = 100 mW) f_{2H} = 0.35–200 MHz f_{2H} = 200–400 MHz	d _{SO}	_ _	-65 —	-60 -50	dB
Peak Envelope Power (Two Tone Distortion Test — See Figure 10) f = 0.35-200 MHz @ -32 dB IMD f = 200-400 MHz @ -32 dB IMD	PEP	600	800	_	mW
Supply Current	Icc	190	205	220	mA

TYPICAL CHARACTERISTICS

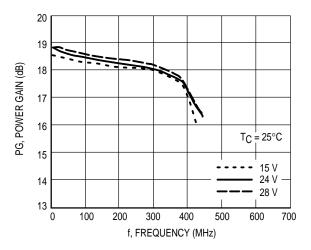


Figure 1. Power Gain versus Voltage

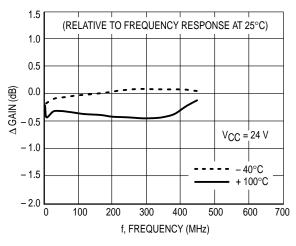


Figure 2. Relative Power Gain versus Temperature

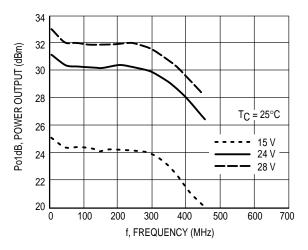


Figure 3. 1 dB Compression versus Voltage

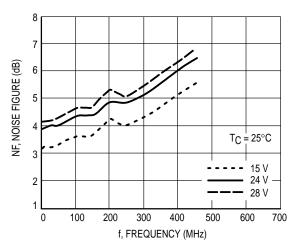


Figure 4. Noise Figure versus Voltage

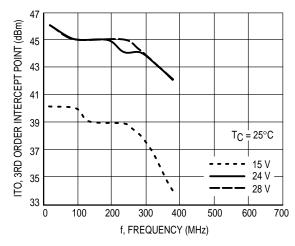


Figure 5. Third Order Intercept versus Voltage

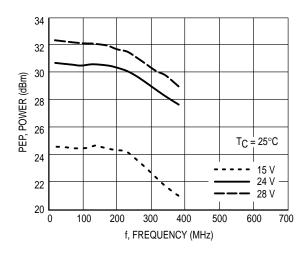
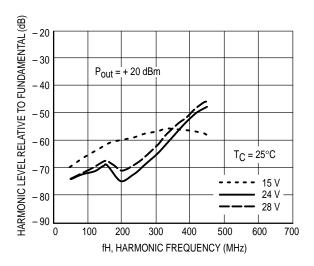


Figure 6. Peak Envelope Power versus Voltage



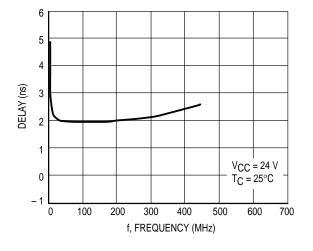


Figure 7. Second Harmonic Distortion versus Voltage

Figure 8. Group Delay versus Frequency

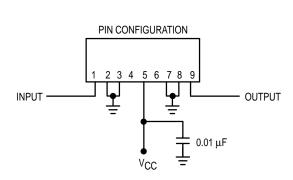
Biased at 24 Volts

 $T = 25^{\circ}C$ $Zo = 50\Omega$

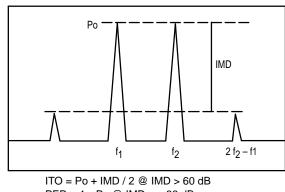
Frequency (MHz)	S11		S21		S12		S22	
	Mag	Ang	Mag	Ang	Mag	Ang	Mag	Ang
0.35	-17.0	18.7	18.4	7.4	-24.1	-169	-16.4	11.1
1	-17.3	10.7	18.6	3.4	-24.0	-175	-16.7	6.5
50	-16.3	-7.6	18.7	-38.8	-23.9	145	-17.0	-38.8
100	-15.6	-15.1	18.5	-70.1	-24.1	117	-18.4	-65.9
200	-14.0	-47.3	18.3	-149	-24.8	47.9	-20.6	-101
300	-14.1	-85	18.1	135	-25.3	-15	-16.6	-142
400	-18.0	-137	17.4	58	-25.9	-84.3	-14.2	134

Magnitude in dB, Phase Angle in degrees.

Table 1. S-Parameters



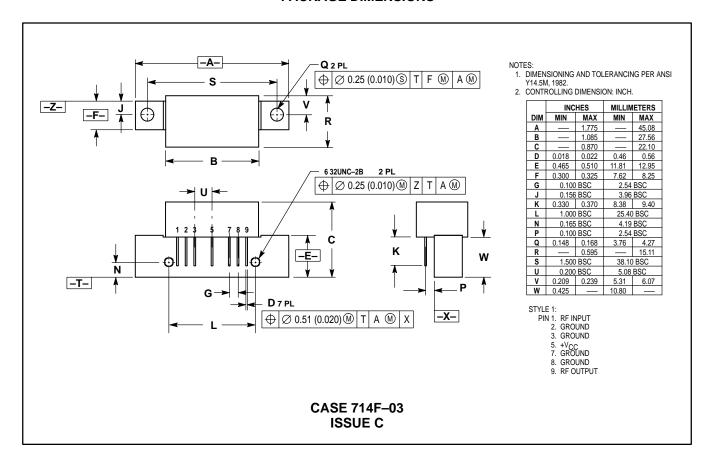




 $PEP = 4 \times Po @ IMD = -32 dB$

Figure 10. Intermodulation Test

PACKAGE DIMENSIONS



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